	Туре	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	4534	(first adj inverter) same (coupl\$3 or connect\$3 or series) same (second adj inverter)	USPAT	2005/01/0 6 08:41
2	BRS	L2		transistor same inverter\$1 same (connect\$3 or coupl\$3) same input same output	USPAT	2005/01/0 6 09:23
3	BRS	L3	2569	1 and 2	USPAT	2005/01/0 6 09:25
4	BRS	L4	872	(delay adj reduc\$4) and buffer	IIISPAT	2005/01/0 6 09:27
5	BRS	L5	19	3 and 4	USPAT	2005/01/0 6 09:27